

Approved
for entry
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DR

ABSTRACT OF THE DISCLOSURE

In order to improve selectivity Selectivity Q of a tuning circuit is maximized by using a negative resistance circuit which that is hard to be not influenced significantly by means of change of use condition changes in conditions such as temperature, source voltage, etc. The tuning circuit operates stably and has simply circuit construction, the tuning circuit is constituted by a series resonance circuit and at the negative resistance circuit connected thereto.

The negative resistance circuit is constituted by can be a C-E dividing type circuit comprising of including a npn transistor as a first stage circuit and an emitter earth type amplifying circuit comprising of including a pnp transistor as a second stage circuit. A collector output of the pnp transistor is connected to an emitter of the npn transistor to constitute a negative feedback circuit and said the collector output is divided and connected to a base of the npn transistor to constitute a positive feedback circuit.

Selectivity Q is improved by the negative resistance circuit provided by the negative feedback circuit.